Simulator Requirement Specification

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| Department Name | Department of Basic Science and Humanities |
| Class | FYBTECH |
| Semester | 01 |
| Subject Name | Basic Electrical Engineering Lab |
| Experiment No. | 01 |
| Experiment Name | RLC circuit with variable inductance and capacitance |

Version History

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| --- | --- | --- | --- | --- |
| Sr. No. | Version Number | Created By | Approved By | Date |
| 1 | v1.0 | Siddhi Patil | Faculty Name | DD/MM/YYYY |
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Detailed Requirement Specification

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| **Req. ID** | **Visual Entity Required** | **Requirement Description** | **Comments** |
| REQ1 | Resistor | Variable (as shown in circuit diagram) | So that one can set any value of resistor |
| REQ2 | Capacitor | Variable (as shown in circuit diagram) |  |
| REQ3 | Inductor | As shown in circuit diagram |  |
| REQ4 | Power Button (230V,50Hz) | Power ON/OFF button |  |
| REQ5 | Display button | To Display voltage and current across each component |  |
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